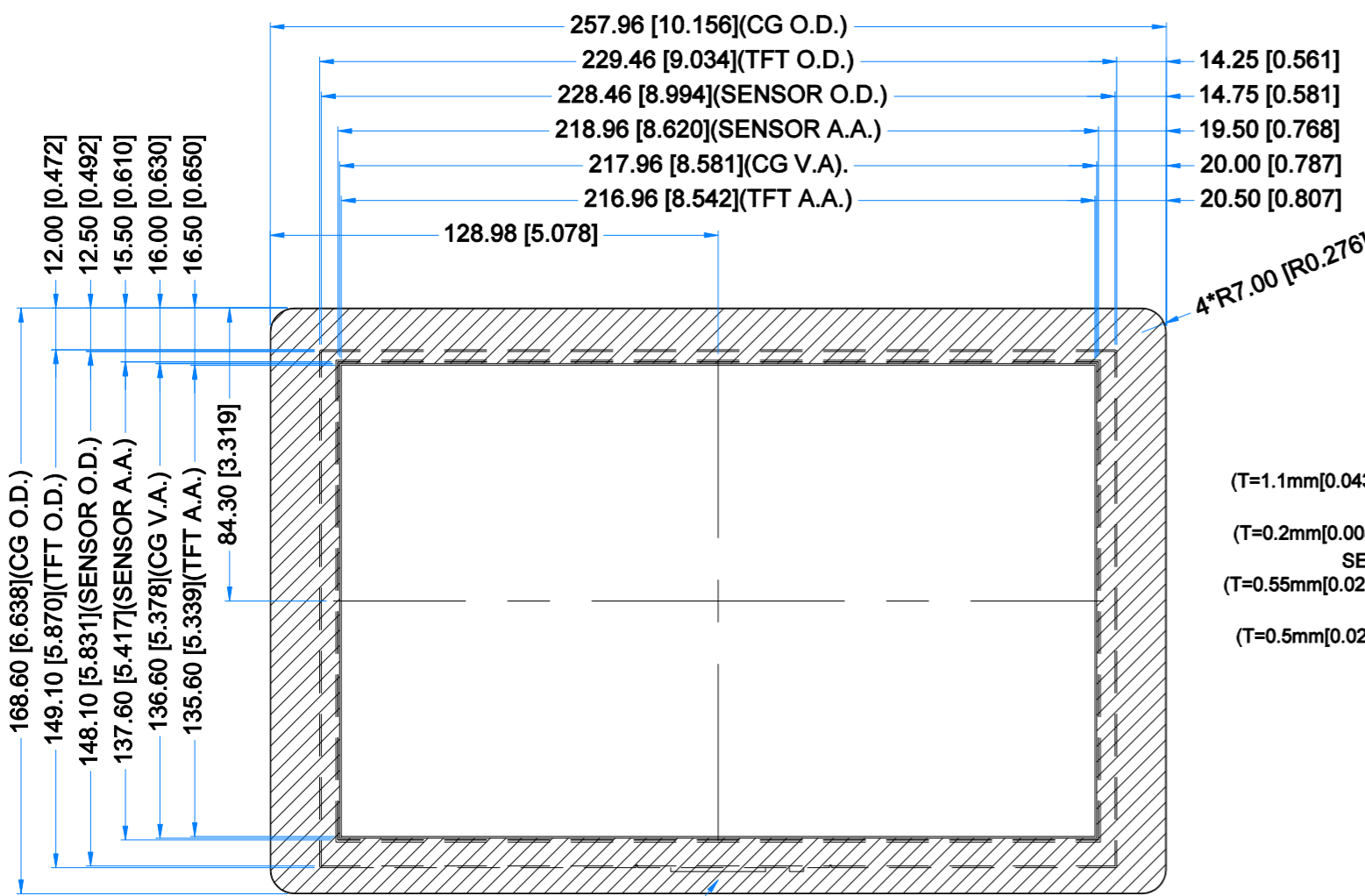
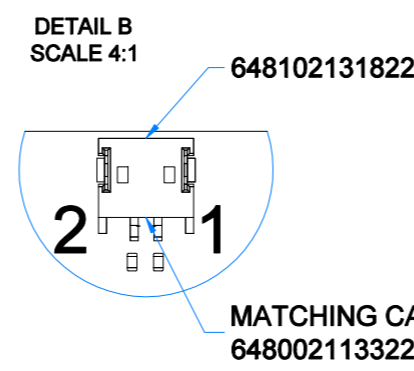
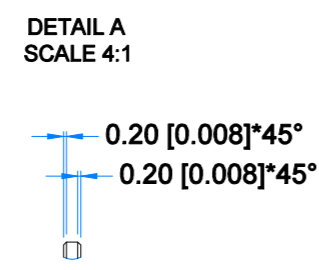


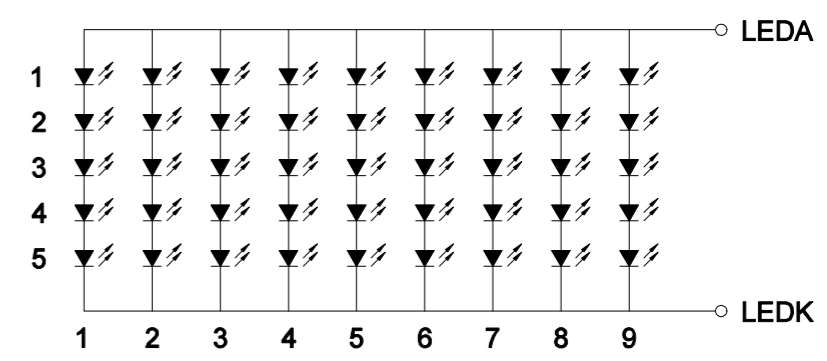
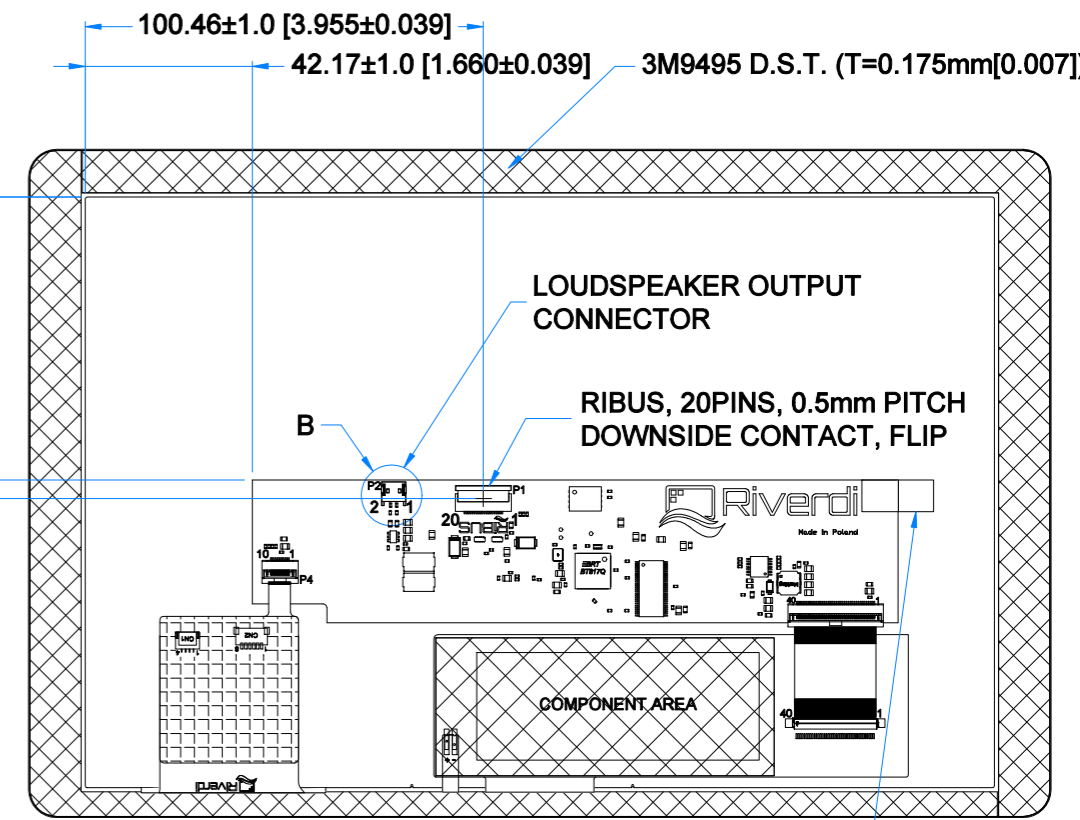
Revision:	Changes:	Date:
1.0	Initial Case	2020.12.09
1.1	PCB Update And Dimension Overhaul	2021.04.20
1.2	Adding Speaker PN And Matching Connector PN	2021.07.08
1.3	PCB Update And Added Grounding Tape And Dimensions Overhaul	2022.08.31



BLACK MASK PRINTING
COLOR: RAL9005



- MAX 13.17 [0.518](MODULE O.D.)
- 6.85 [0.270](CTP+TFT)
- 2.35 [0.093](CG+SCA+SENSOR+DST)
- 1.28 [0.050](CG+DST)
- 1.10 [0.043](CG)



LED Diagram Circuit
PINOUT ON THE 2ND PAGE

LCD NOTES:

- LCD TYPE: TRANSMISSIVE, NORMALLY BLACK, IPS
- RESOLUTION: 1280x800
- VIEWING ANGLE: FREE
- SURFACE LUMINANCE: 800 cd/m²
- DRIVING IC ON THE BOARD: BT817Q
- INTERFACE: SPI/QSPI
- SUPPLY VOLTAGE FOR MODULE: 3.3 V
- SUPPLY VOLTAGE FOR BACKLIGHT: 7.0 + 14.0 V, BUILT-IN LED INVERTER

TP NOTES:

- TP STRUCTURE: G+G
- CG THICKNESS: 1.10mm[0.043inch]
- SURFACE HARDNESS: 7H
- DRIVER IC: ILI2132A
- INTERFACE: SPI/QSPI VIA RIBUS AND BT817Q

GENERAL NOTES:

- ZERO BAD PIXEL FOR TFT
- AIR BONDING BETWEEN TFT AND CTP
- OPERATING TEMPERATURE: -20°C ~ 70°C
- STORAGE TEMPERATURE: -30°C ~ 80°C
- WITHOUT INDIVIDUAL TOLERANCE: ±0.3mm[0.012inch]
- RoHS3 COMPLIANT

PN: RVT101HVBNWC00
SN:
DRAWN: M.Natywa
CHECKED: Carol Gao
APPR:

2022.08.31 1:1.91
2022.09.13 [mm]
ISO A3 P. 1 of 1



P1-RIBUS CONNECTOR			
Pin No.	Desc.	Pin No.	Desc.
1	VDD	11	GPIO.1/IO.2
2	GND	12	GPIO.2/IO.3
3	SPI_SCLK	13	NC
4	MISO/IO.1	14	NC
5	MOSI/IO.0	15	NC
6	CS	16	NC
7	INT	17	BLVDD
8	RST/PD	18	BLVDD
9	GPIO.0	19	BLGND
10	DISP_AUDIO	20	BLGND

P2-LOUDSPEAKER OUTPUT CONNECTOR			
Pin No.	Desc.	Pin No.	Desc.
1	SPEAKER+	2	SPEAKER-